

WEST Search History

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DATE: Friday, February 06, 2004



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		<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L22	((('6032274') [ABPN1,NRPN,PN,TBAN,WKU]) and @pd > 20030916	0
<input type="checkbox"/>	L21	(L20 and clock edge with (compress\$3 or compact\$3) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7 or stor\$4 or register\$4)) and @pd > 20030916	0
<input type="checkbox"/>	L20	(L19 and clock edge with (compress\$3 or compact\$3)) and @pd > 20030916	0
<input type="checkbox"/>	L19	(clock edge) and @pd > 20030916	358
<input type="checkbox"/>	L18	(L15 and L16) and @pd > 20030916	0
<input type="checkbox"/>	L17	(L15 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) and @pd > 20030916	0
<input type="checkbox"/>	L16	(L14 and (compress\$4 or compact\$4) same (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) and @pd > 20030916	0
<input type="checkbox"/>	L15	(L14 and (compress\$4 or compact\$4) and (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4)) and @pd > 20030916	0
<input type="checkbox"/>	L14	(L13 and (compress\$4 or compact\$4) and (pull\$1up or pull\$1down)same latch\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) and @pd > 20030916	2
<input type="checkbox"/>	L13	((compress\$4 or compact\$4) and (pull\$1up or pull\$1down)same latch\$4) and @pd > 20030916	24
<input type="checkbox"/>	L12	(L1 and (pull\$1up or pull\$1down)same latch\$4) and @pd > 20030916	0
<input type="checkbox"/>	L11	(L7 and (pull\$1up or pull\$1down)same latch\$4) and @pd > 20030916	0
<input type="checkbox"/>	L10	((pull\$1up or pull\$1down)same latch\$4) and @pd > 20030916	120
<input type="checkbox"/>	L9	((('6163491') [ABPN1,NRPN,PN,TBAN,WKU]) and @pd > 20030916	0
<input type="checkbox"/>	L8	(L7 and double data rate same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) and @pd > 20030916	0
<input type="checkbox"/>	L7	(L6 and double data rate and (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7) and (clock or timing)) and @pd > 20030916	4

<input type="checkbox"/>	L6	(double data rate and (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) and @pd > 20030916	6
<input type="checkbox"/>	L5	(L1 and (double near (data or rate) or ddr)same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) and (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) and @pd > 20030916	1
<input type="checkbox"/>	L4	((('6324602') [ABPN1,NRPN,PN,TBAN,WKU]) and @pd > 20030916	0
<input type="checkbox"/>	L3	((('6317372') [ABPN1,NRPN,PN,TBAN,WKU]) and @pd > 20030916	0
<input type="checkbox"/>	L2	(L1 and (double near (data or rate) or ddr)same (compress\$4 or compact\$4) same (test\$3 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or memor\$7)) and @pd > 20030916	1
<input type="checkbox"/>	L1	((double near (data or rate) or ddr)same (compress\$4 or compact\$4)) and @pd > 20030916	24

END OF SEARCH HISTORY